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Selected Areas in Communications, IEEE Journal on , Volume: 18 Issue: 6
Page(s): 829 -840

[\[Abstract\]](#) [\[PDF Full-Text \(232 KB\)\]](#) **JNL**

2 Fast modeling of core switching noise on distributed LRC power circuits

Zheng, L.-R.; Tenhunen, H.

Electrical Performance of Electronic Packaging, 2000, IEEE Conference on.
Page(s): 307 -310

[\[Abstract\]](#) [\[PDF Full-Text \(292 KB\)\]](#) **CNF**

3 Effects of global interconnect optimizations on performance estimation in deep submicron design

Yu Cao; Chenming Hu; Xuejue Huang; Kahng, A.B.; Muddu, S.; Stroobant; Sylvester, D.

Computer Aided Design, 2000. ICCAD-2000. IEEE/ACM International Conference on
2000

Page(s): 56 -61

[\[Abstract\]](#) [\[PDF Full-Text \(588 KB\)\]](#) **CNF**

4 Buffer insertion for noise and delay optimization

Alpert, C.J.; Devgan, A.; Quay, S.T.

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transact
Volume: 18 Issue: 11 , Nov. 1999
Page(s): 1633 -1645

[\[Abstract\]](#) [\[PDF Full-Text \(440 KB\)\]](#) **JNL**

**5 Voice over wireless Internet: performance interaction of signal p
algorithms and network protocols**

Schurgers, C.; Srivastave, M.B.

Vehicular Technology Conference, 1999 IEEE 49th , Volume: 3 , 1999

Page(s): 1935 -1939 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(428 KB\)\]](#) **CNF**

6 Buffer insertion for noise and delay optimization

Alpert, C.J.; Devgan, A.; Quay, S.T.

Design Automation Conference, 1998. Proceedings , 1998

Page(s): 362 -367

[\[Abstract\]](#) [\[PDF Full-Text \(604 KB\)\]](#) **CNF**

**7 Joint source/channel coding of statistically multiplexed real-time
packet networks**

Garrett, M.W.; Vetterli, M.

Networking, IEEE/ACM Transactions on , Volume: 1 Issue: 1 , Feb. 1993

Page(s): 71 -80

[\[Abstract\]](#) [\[PDF Full-Text \(928 KB\)\]](#) **JNL**

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Solid-State Circuits, IEEE Journal of, Volume: 36 Issue: 8, Aug. 2001

Page(s): 1239 -1249

[\[Abstract\]](#) [\[PDF Full-Text \(308 KB\)\]](#) **JNL****2 A high-speed CMOS track/hold circuit***Mohamed Zin, M.A.; Kobayashi, H.; Kobayashi, K.; Ichimura, J.-I.; Hao S; Kimura, Y.; Yuminaka, Y.; Sasaki, Y.; Tanaka, K.; Abe, F.*

Electronics, Circuits and Systems, 1999. Proceedings of ICECS '99. The 6th International Conference on, Volume: 3, 1999

Page(s): 1709 -1712 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(308 KB\)\]](#) **CNF****3 A CMOS dual channel, 100 MHz-1.1 GHz transmitter for cable app***Borremans, M.; De Ranter, C.; Steyaert, M.*

Solid-State Circuits Conference, 1999. Digest of Technical Papers. ISSCC. International, 1999

Page(s): 164 -165

[\[Abstract\]](#) [\[PDF Full-Text \(252 KB\)\]](#) **CNF****4 Accurate modeling of simultaneous switching noise in low voltage VLSI***Sang Won Song; Ismail, M.; Gyu Moon; Dong Yong Kim*

Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE Inte

Symposium on , Volume: 6 , 1999
Page(s): 210 -213 vol.6

[\[Abstract\]](#) [\[PDF Full-Text \(248 KB\)\]](#) [CNF](#)

5 Design of a slew rate controlled output buffer

Garcia, F.; Coll, P.; Anvergne, D.

ASIC Conference 1998. Proceedings. Eleventh Annual IEEE International ,
Page(s): 147 -150

[\[Abstract\]](#) [\[PDF Full-Text \(396 KB\)\]](#) [CNF](#)

6 Performance analysis of MCM systems

Truzzi, C.; Beyne, E.; Ringoot, E.

Components, Packaging, and Manufacturing Technology, Part B: Advanced
IEEE Transactions on [see also Components, Hybrids, and Manufacturing
IEEE Transactions on] , Volume: 20 Issue: 3 , Aug. 1997
Page(s): 334 -341

[\[Abstract\]](#) [\[PDF Full-Text \(200 KB\)\]](#) [JNL](#)

7 Effects of simultaneous switching noise on the tapered buffer de

Vemuru, S.R.

Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volu
3 , Sept. 1997
Page(s): 290 -300

[\[Abstract\]](#) [\[PDF Full-Text \(780 KB\)\]](#) [JNL](#)

**8 Forming damped LRC parasitic circuits in simultaneously switche
output buffers**

Cabara, T.J.; Fischer, W.C.; Harrington, J.; Troutman, W.W.

Solid-State Circuits, IEEE Journal of , Volume: 32 Issue: 3 , March 1997
Page(s): 407 -418

[\[Abstract\]](#) [\[PDF Full-Text \(520 KB\)\]](#) [JNL](#)

**9 MCM-D for high speed digital applications-electrical modelling ar
performance evaluation**

Truzzi, C.; Beyne, E.; Ringoot, E.

Multichip Modules, 1997., International Conference on , 1997
Page(s): 29 -34

[\[Abstract\]](#) [\[PDF Full-Text \(624 KB\)\]](#) **CNF**

10 Time and frequency domain analysis of integral decoupling capacitors
Goetz, M.P.

Components, Packaging, and Manufacturing Technology, Part B: Advanced Packaging Technology, IEEE Transactions on [see also Components, Hybrids, and Manufacturing Technology, IEEE Transactions on] , Volume: 19 Issue: 3 , Aug. 1996
Page(s): 518 -522

[\[Abstract\]](#) [\[PDF Full-Text \(436 KB\)\]](#) **JNL**

11 Improvements in dynamic and noise performance of cryogenic CMOS monolithic ASICs

Camin, D.V.; Fedyakin, N.; Pessina, G.; Previtali, E.; Sironi, M.
Nuclear Science, IEEE Transactions on , Volume: 43 Issue: 3 Part: 2 , June 1996
Page(s): 1649 -1655

[\[Abstract\]](#) [\[PDF Full-Text \(896 KB\)\]](#) **JNL**

12 SSN effects on tapered buffers

Vemuru, S.R.
Circuits and Systems, 1996., IEEE 39th Midwest symposium on , Volume: 1
Page(s): 25 -28 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(364 KB\)\]](#) **CNF**

13 A feedback control circuit design technique to suppress power MOSFET switching speed output driver

Choy, C.S.; Chan, C.F.; Ku, M.H.
Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium on Circuits and Systems, Volume: 1 , 1995
Page(s): 307 -310 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(340 KB\)\]](#) **CNF**

14 Effect of CMOS driver loading conditions on simultaneous switching noise
Vaidyanath, A.; Thoroddsen, B.; Prince, J.L.

Components, Packaging, and Manufacturing Technology, Part B: Advanced Packaging Technology, IEEE Transactions on [see also Components, Hybrids, and Manufacturing Technology, IEEE Transactions on] , Volume: 17 Issue: 4 , Nov. 1994
Page(s): 480 -485

[\[Abstract\]](#) [\[PDF Full-Text \(440 KB\)\]](#) **JNL**

15 Enhanced transceiver logic*Filliter, K.B.*

WESCON/94. Idea/Microelectronics. Conference Record , 1994

Page(s): 606 -608

[\[Abstract\]](#) [\[PDF Full-Text \(200 KB\)\]](#) [CNF](#)**16 Designing with high speed clock drivers***Hronik, S.*

WESCON/'93. Conference Record, , 1993

Page(s): 169 -175

[\[Abstract\]](#) [\[PDF Full-Text \(588 KB\)\]](#) [CNF](#)**17 A VLSI-based biological interface for extracellular potentials***Osborn, M.J.; Wolpert, S.*

Bioengineering Conference, 1993., Proceedings of the 1993 IEEE Nineteen Northeast , May 1993

Page(s): 184 -186

[\[Abstract\]](#) [\[PDF Full-Text \(240 KB\)\]](#) [CNF](#)**18 A VLSI-based biological interface for prosthetic devices***Osborn, M.J.; Wolpert, S.*

Bioengineering Conference, 1992., Proceedings of the 1992 Eighteenth IEI Northeast , 1992

Page(s): 75 -76

[\[Abstract\]](#) [\[PDF Full-Text \(152 KB\)\]](#) [CNF](#)**19 An experimental 1.5-V 64-Mb DRAM***Nakagome, Y.; Tanaka, H.; Takeuchi, K.; Kume, E.; Watanabe, Y.; Kaga, Kawamoto, Y.; Murai, F.; Izawa, R.; Hisamoto, D.; Kisu, T.; Nishida, T.; Itoh, K.*

Solid-State Circuits, IEEE Journal of , Volume: 26 Issue: 4 , April 1991

Page(s): 465 -472

[\[Abstract\]](#) [\[PDF Full-Text \(808 KB\)\]](#) [JNL](#)**20 New full-voltage-swing BiCMOS buffers***Embabi, S.H.K.; Bellaouar, A.; Elmasry, M.I.; Hadaway, R.A.*

Solid-State Circuits, IEEE Journal of , Volume: 26 Issue: 2 , Feb. 1991

Page(s): 150 -153

[\[Abstract\]](#) [\[PDF Full-Text \(348 KB\)\]](#) [JNL](#)

21 CMOS output buffer waveshaping

Albertson, L.; Whitaker, S.; Merrell, R.

VLSI, 1991. Proceedings., First Great Lakes Symposium on , 1991

Page(s): 326 -327

[\[Abstract\]](#) [\[PDF Full-Text \(164 KB\)\]](#) [CNF](#)

22 pi -heterostructure field effect transistors for VLSI applications

Lee, K.; Shur, M.

Electron Devices, IEEE Transactions on , Volume: 37 Issue: 8 , Aug. 1990

Page(s): 1810 -1820

[\[Abstract\]](#) [\[PDF Full-Text \(852 KB\)\]](#) [JNL](#)

23 An 8-bit 2-ns monolithic DAC

Kamoto, T.; Akazawa, Y.; Shinagawa, M.

Solid-State Circuits, IEEE Journal of , Volume: 23 Issue: 1 , Feb. 1988

Page(s): 142 -146

[\[Abstract\]](#) [\[PDF Full-Text \(460 KB\)\]](#) [JNL](#)

24 A high frequency GaAs multivibrator IC

Kobayashi, M.; Tanino, N.; Komaru, M.; Shimura, T.; Noda, M.; Ishibara,
Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 1988. Technica
1988., 10th Annual IEEE , 1988

Page(s): 151 -154

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1 I/O buffer placement methodology for ASICs*Kozhaya, J.N.; Nassif, S.R.; Najm, F.N.*Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE Intern
Conference on , Volume: 1 , 2001

Page(s): 245 -248

[\[Abstract\]](#) [\[PDF Full-Text \(360 KB\)\]](#) **CNF**

**2 Effects of global interconnect optimizations on performance estim
deep submicron design***Yu Cao; Chenming Hu; Xuejue Huang; Kahng, A.B.; Muddu, S.; Stroobanc
Sylvester, D.*Computer Aided Design, 2000. ICCAD-2000. IEEE/ACM International Conf
2000

Page(s): 56 -61

[\[Abstract\]](#) [\[PDF Full-Text \(588 KB\)\]](#) **CNF**

3 A practical repeater insertion method in high speed VLSI circuits*Culetu, J.; Amir, C.; MacDonald, J.*

Design Automation Conference, 1998. Proceedings , 1998

Page(s): 392 -395

[\[Abstract\]](#) [\[PDF Full-Text \(392 KB\)\]](#) **CNF**

4 CAD tools for area-distributed I/O pad packaging*Farbarik, R.; Xiaowen Liu; Rossman, M.; Parakh, P.; Basso, T.; Brown, R.*

Multi-Chip Module Conference, 1997. MCMC '97., 1997 IEEE , 1997

[\[Abstract\]](#) [\[PDF Full-Text \(812 KB\)\]](#) **CNF**

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[\[Abstract\]](#) [\[PDF Full-Text \(748 KB\)\]](#) **CNF**

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[\[Abstract\]](#) [\[PDF Full-Text \(604 KB\)\]](#) **CNF**

Page(s): 483 -486

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) **CNF**

10 **Buffer placement in distributed RC-tree networks for minimal EI**
van Ginneken, L.P.P.P.
Circuits and Systems, 1990., IEEE International Symposium on , 1990
Page(s): 865 -868 vol.2

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